CIRRICULAM VITAE

HAJIRA FATHIMA

B.E (Electronics & Communication), M.E (Systems & Signal Processing), Pursuing Ph.D

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Career Objective:

To pursue a challenging career in the field of teaching and be apart of a progressive organization that gives me a scope to enhance my knowledge and skills.

EDUCATIONAL QUALIFICATION

- **Pursuing Ph.D** from Maulana Azad National Urdu University.
- **Master of Engineering (Signals & System Processing)** from "Osmania University", Hyderabad.
- **Bachelor of Engineering (Electronics & Communication)** from "Muffakham Jah College of Engineering and Technology", affiliated to Osmania University, Hyderabad.
- **Diploma in Electronics & Communication Engineering from** "State Board of Technical Education and Training", Hyderabad.
- **SSC** from **"**Board of Secondary Education", Hyderabad.

WORK EXPERIENCE (FOURTEEN YEARS)

- Working as an Assistant Professor in E.C.E Department, MANUU Polytechnic, Hyderabad Since 21st May 2012 to till date.

Softwares known:

- Operating System : MS-Dos, Window-XP, Linux
- Programming Language : C, Verilog HDL, VHDL, Linux Kernel Programming
- Software packages : PSPICE, Multisim, MATLAB/Simulink, Code Composer Studio, Xilinx ISE 8.2i

ACADEMIC PROJECTS

M.E Project:

Title	:	**Hidden Markov Modeling using Sub band – MFCCs For Speech Recognition**
Environment used	:	MATLAB
Period	:	1 year

Description of the Project:

In this project a Speech Recognition Model using Sub band –MFCC for feature extraction & Hidden Markov Models for Recognition will be developed. By using these two techniques for speech recognition the recognition accuracy will be increased as well as the no. of iterations required for recognition will also be reduced.

B.E Project:

Title	:	**Simulation of "UNIVERSAL SYNCHRONOUS & ASYNCHRONOUS RECEIVER TRANSMITTER"**
Environment used	:	VHDL.
Duration	:	4 months

Description of the Project:

The USART is a Serial communications Interface & this project has been developed to enable data communication between CPU and external devices. This project has been done in the 4th Year, 2nd Semester of B.E.

WORKSHOPS/SUMMER -SCHOOL COURSES ATTENDED

Event	Topic	Place	Duration
Workshop	Research Methodology	University College of Engineering, Osmania University	3 – Days 26-08-2013 to 28-08-2013
Orientation Programme	-	UGC – HRDC JNTU Hyderabad	4 – Weeks 08-02-2016 to 05-03-2016
Workshop	Research Methodology	BITS Pilani Hyderabad Campus	2 – Days 11-04-2016 to 12-04-2016
Induction Programme	-	UGC – HRDC AMU, Aligarh	7 – Days 17-10-2016 to 23-10-2016
FDP	Wireless and Mobile Communication	Electronics & ICT Academy, NIT –	1- Week 01-07-2019 to 06-07-2019

		Warangal at VNR VJIET, Hyderabad.	
FDP	Sensor Networks, Internet of Things and Internet of Everything	Dept. of CSIT, School of Technology, MANUU, Gachibowli, Hyd., Telangana.	2 – Weeks 25-11-2019 to 08-12-2019
ATAL Academy FDP	Artificial Intelligence	MANUU, Hyderabad	1- Week 16-12-2019 to 20-12-2019
Industrial Training	Fiber Optic Communication	Regional Telecom Training Centre (RTTC), Bharat Sanchar Nigam Limited (BSNL), Hyderabad.	3- Weeks 16-11-2020 to 05-12-2020
FDP	Advanced Pedagogy	RTTC, BSNL, Hyderabad.	1-Week 08-02-2021 to 14-02-2021
Refresher Course	Computer Science and Information Technology	UGC-HRDC MANUU Hyderabad	2-Weeks 21-07-2022 to 03-08-2022
Online FDP	Reimagining the future: Cutting-edge Innovations in Artificial Intelligence and Machine Learning	Department of Information Technology, LORDS Institute of Engineering and Technology, Hyderabad	1-Week 15-12-2025 to 20-12-2025
Capacity Building program	Integration of Indian Knowledge System into Curriculum	MMTTC-UGC MANUU, Hyderabad	1-Week 05-01-2026 to 10-01-2026

DEPARTMENT ACTIVITIES

- **Inchagre Head of** E.C.E Department, MANUU Polytechnic, Hyderabad from 21st May 2012 to 29th April 2015.
- **Inchagre Head of** E.C.E Department, MANUU Polytechnic, Hyderabad from 10th November 2021 to till date.
- Project Coordinator for ECE Dept. in Azad Tek Fest -2012
- Project Coordinator for ECE Dept. in Azad Tek Fest -2013
- Project Coordinator for ECE Dept. in Azad Tek Fest -2014
- Co-coordinator for Azad Tek Fest -2015
- Coordinator for Azad Tek Fest -2020
- Incharge of Circuits Lab
- Incharge of Industrial Visits in ECE Dept.
- Established ECE Laboratories.
- In charge of Industrial Training in ECE Dept.

PROJECTS GUIDED

M.E Project:

- A High Performance FFT Architecture.

B.E Projects:

- Implementation of Adaptive Noise Canceller using DSP Processor-TMS320C6713
- Real time Implementation of Audio Signal affects using simulink and DSP Processor-TMS320C6713
- Implementation of Speaker Recognition System using DSP Proceeos-TMS320C6713
- Implementation of Distributed Arithmetic FIR Filter On FPGA.
- Simulation of Formant Frequency Estimator for speech signals
- Simulation of MFCC Extraction for speech signals.

PAPERS PUBLISHED

- Hajira Fathima, "Design and Development of a Robust Phase Shifter Algorithm for Adaptive Digital Beam Forming", Design Engineering Journal, Issue -9, 2021 Pages: 17547 – 17558.

SUBJECT OF INTEREST/HANDLED

- Electronics Devices and Circuits.
- Analog Electronic Circuits.
- Digital Electronics.
- Mechatronics
- Pulse Digital & Switching Circuits.
- Microcontroller and Microprocessors
- Digital Signal Processing.
- Speech Processing.
- Signals and Systems.
- Veilog HDL.
- Artificial Intelligence
- Machine Learning
- Deep Learning

DECLARATION

I hereby declare that the above written particulars are true to the best of my knowledge and belief.

Date:18/02/2026
Place: Hyderabad **(HAJIRA FATHIMA)**